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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/663,340	09/15/2000	Kimimori Hamada	PM 271420	1868
909	7590	02/14/2002	EXAMINER	
PILLSBURY WINTHROP LLP 1600 TYSONS BOULEVARD MCLEAN, VA 22102			MONDT, JOHANNES P	
ART UNIT		PAPER NUMBER		
		2826		

DATE MAILED: 02/14/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/663,340	HAMADA, KIMIMORI
Examiner	Art Unit	
Johannes P Mondt	2826	

*The MAILING DATE of this communication appears on the cover sheet with the correspondence address --*

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION

- THE MAILING DATE OF THIS COMMUNICATION.** Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status



## Disposition of Claims

- 4)  Claim(s) 1,3-13,15 and 16 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 1,3-13,15 and 16 is/are rejected.

7)  Claim(s) \_\_\_\_\_ is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)  The proposed drawing correction filed on \_\_\_\_\_ is: a)  approved b)  disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.

12)  The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)  All b)  Some \* c)  None of:

  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14)  Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a)  The translation of the foreign language provisional application has been received.

15)  Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_ .

4)  Interview Summary (PTO-413) Paper No(s) \_\_\_\_ .  
5)  Notice of Informal Patent Application (PTO-152)  
6)  Other: \_\_\_\_ .

## DETAILED ACTION

### ***Response to Amendment***

Amendment A has been filed 01/18/02 and entered as Paper No. 6. Applicants canceled claims 2 and 14. The objections previously made have been removed with the exception of the persistent occurrence of "ladder-like" in the claim language (amended claims 3 and 15). It is noted that, by the way in which said objections have been removed, redundancies between several claims have been created: particularly, claims 5 and 6 are identical, and so are claims 9 and 10.

Since the previous, first Detailed Action, new prior art has been found, on which this, second, Detailed Action, is based.

### ***Drawings***

1. The drawings are objected to because
  - (a) the phrase "atmos" in the ordinate of Figure 3 should be replaced by "atoms", while
    - (b) the phrase "GRATE RESISTANCE" in Figure 4B should be replaced by "GREAT RESISTANCE". A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

### ***Claim Objections***

1. *Claims 6 and 10 are objected to because of the following informalities: Claim 6 is identical claim 5 and therefore should be omitted. Claim 10 is identical to claim 9 and therefore should be omitted. Appropriate correction is required.*

### ***Claim Rejections - 35 USC § 103***

1. *The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:*

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. *Claims 1, 3-13, and 15-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamada (5,502,320) in view of Hshieh et al (5,986,304).*  
*With regard to claim 1: Yamada teaches (cf. Fig. 6B) a semiconductor device (cf. title) comprising a substrate 1 (cf. column 5, lines 8-9) having a major surface and of first conductivity type (p type);*

*a plurality of trench gates 5 (cf. Figs. 6A and 6B);  
a plurality of first semiconductor regions of second conductivity type (n-type) (the vertical portions of the diffusion layers 4; cf. column 5, line 32) different from the first conductivity type, the first semiconductor regions having a first depth as measured from said major surface, at least a portion of the first*

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semiconductor regions flanking the trench gates on both of their sides and being in contact with said trench gates via films (cf. column 7, line 54) bordering and insulating the trench gates; and

a plurality of second semiconductor regions of the second conductivity type (those horizontal portions of the diffusion layers 4 that do not flank the trench gates; cf. column 5, line 32) having a second depth as measured from said major surface of said substrate that is less than the first depth, wherein the second semiconductor regions connect the plurality of first semiconductor regions spaced apart from each other.

*Yamada does not necessarily teach the semiconductor device to comprise a body region of first conductivity type formed in a semiconductor substrate such that said plurality of trench gates extend through said body region. However, the device by Yamada is not necessarily designed to withstand high voltage. In the case of the power semiconductor device of Applicants, *punch-through prevention* is an obviously desirable feature, and, as shown for instance by Hsie et al (5,986,304) the use of a body region 120 (cf. Fig. 3A and column 4, line 25) of first conductivity type formed in the substrate such that the plurality of trench gates extend through said body region is a standard design to prevent punch-through.*

*It therefore would have been obvious to one of ordinary skills to modify the invention at the time it was made so as to include a body region of a first conductivity type formed in said substrate and having a major surface opposite to*

the surface shared by said substrate and the body region, such that the aforementioned plurality of trench gates extend through the aforementioned body region.

*With regard to claim 2:* Yamada teaches the semiconductor device of claim 1 wherein the first semiconductor regions are formed along the trench gates 5 (cf. Figs. 6A and 6B), and the second semiconductor regions connect the first semiconductor regions formed between the trench gates so as to form a ladder-shaped configuration.

*With regard to claim 4:* Yamada teaches the semiconductor device of claim 1 wherein the first semiconductor regions are formed along the trench gates 5 (cf. Figs. 6A and 6B), and the second semiconductor regions connect the first semiconductor regions formed between the trench gates so as to form a ladder-like configuration.

*With regard to claim 5 – 8:* The semiconductor device as essentially taught by Yamada and Hsieh et al of either claim 1, claim 3, or claim 4 has a wiring or wiring member 10 connected to at least one of the plurality of trench gates or gate electrodes 7 (cf. column 6, line 44-47).

*With regard to claims 9 – 12:* wiring 10 and gate electrode 7, respectively connected with the second semiconductor regions and the body region (through film 11) are connected (cf. column 6, lines 44-47) and thus together form the wiring member of claims 9-12.

*With regard to claims 13 and 15 – 16: The devices of claims 1, 3, and 4 would necessarily have to be formed in order to function. Claims 13, 15-16 fail to further limit the devices of claims 1, 3, and 4 other than simply form each of their components.*

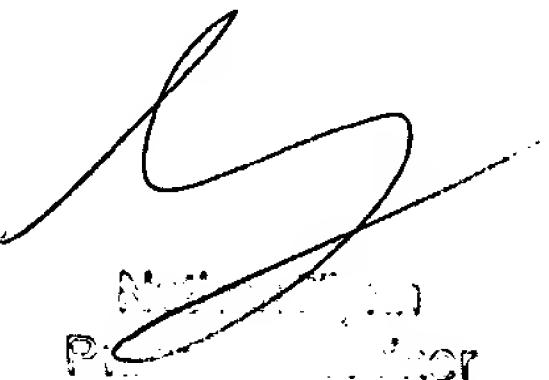
***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Johannes P Mondt whose telephone number is 703-306-0531. The examiner can normally be reached on 8:00 - 18:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached on 703-308-6601. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

JPM  
February 7, 2002

  
Johannes P. Mondt  
Patent Examiner